

COMBINED DECLARATION AND POWER OF ATTORNEY (宣誓書及び委任状)

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name, I believe I am the original, first and sole inventor (if only one name is listed bleow) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

SEMICONDUCTOR INTEGRATED CIRCUIT

the specification of which: (check	one) is attached	hereto.	
	, was filed or		·
		ion Serial No.	
	and was an	nended on	
		(if applicable)	
I hereby state that I have reviewed claims, as amended by any amedme		contents of the above-identified	specification. including the
I acknowledge the duty to disclose with Title 37, Code of Federal Reg		naterial to the examination of thi	s application in accordance
I hereby claim foreign priority ben- patent or inventor's certificate listed certificate having a filing date before	below and have also ide	entified below any foreign applica	v foreign application(s) for tion for patent or inventor's
Prior Foreign Application(s)			Priority Claimed
220083 / 1982 (Number)	Japan	17/December/1982	2 🛱 🗆
(Number)	(Country)	(Day/Month/Year Filed)	Yes No
(Number)	(Country)	(Day/Month/Year Filed)	Yes No
(Number)	(Country)	(Day/Month/Year Filed)	Yes No
I hereby claim the benefit under and, insofar as the subject matter of application in the manner provided duty to disclose material information between the filing date of the prior Application Serial No.)	f each of the claims of by the first paragraph n as defined in Title application and the na (Filing Date)	this application is not disclosed of Title 35, United States Code 37, Code of Federal Regulations tional or PCT international filing (Status) (patented, pending, a	in the prior United States s, \$112, I acknowledge the s, \$1.56(a) which occurred s date of this application:
(Application Serial No.)	(Filing Date)	(Status) (patented, pending, a	oandoned)

I hereby appoint as cipal attorneys: Donald R. Antonelli, Reg. 1,296; David T. Terry Reg. No. 20.178; Charles L. Wands, Reg. No. 25.406; Melvin Kraus, Reg. No. 22,466; James F. Keown, Reg. No. 25.406; Stanley A. Wal, Reg. No. 26,432; David S. Safran, Reg. No. 27,997; William I. Solomon, Reg. No. 28,565; Gregory E. Montone, Reg. No. 28,141; and Ronald J. Shore, Reg. No. 28,577 to prosecute and transact all business connected with this application and any related United States application and international applications. Please direct all communications to the following address:

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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